

S-28-L

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION OF

YAMADA et al

Appln. No.: 09/993,967

Filed: November 27, 2001



Group Art Unit: 2812

Examiner: to be assigned

Atty Dkt 44020/284032

C# M#

Title: SEMICONDUCTOR DEVICE AND
METHOD OF FABRICATING THE SAME

Date: April 18, 2002

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INFORMATION DISCLOSURE STATEMENT

I hereby certify that each enclosed document listed on the herewith Form PTO-1449 was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months ago by the attached European Communication (Search Report Office Action) dated March 21, 2002. See that communication for information on each document.

Note: The attached communication was not received by any individual designated in Section 1.56(c) more than 30 days prior to the filing of this IDS.

This IDS is intended to be in full compliance with the rules, but should the Examiner find any part of its required content to have been omitted, prompt notice to that effect is earnestly solicited, along with additional time under Rule 97(f), to enable Applicant to comply fully.

This Information Disclosure Statement is intended to be in full compliance with the rules, but should the Examiner find any part of its required content to have been omitted, prompt notice to that effect is earnestly solicited, along with additional time under Rule 97(f), to enable Applicant to comply fully.

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YAMADA et al.
09/993,967

Consideration of the foregoing and the enclosure plus the return of a copy of the enclosed Form PTO-1449 with the Examiner's initials in the left column per MPEP 609 along with an early action on the merits of this application are earnestly solicited.

Respectfully submitted,

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